



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/955,632
 Filing Date September 18, 2001
 Inventor Kunal R. Parekh et al.
 Assignee Micron Technology, Inc.
 Group Art Unit 2813
 Examiner Unassigned
 Attorney's Docket No. MI22-1816
 Title: Capacitors, DRAM Arrays, Monolithic Integrated Circuits, And Methods of
 Forming Capacitors, DRAM Arrays, And Monolithic Integrated Circuits

RESPONSE TO OCTOBER 15, 2001 NOTICE TO FILE
CORRECTED APPLICATION PAPERS UNDER 37 CFR 1.152(a)

To: Assistant Commissioner for Patents
 Washington, D.C. 20231

From: Frederick M. Fliegel, Ph.D.
 (Tel. 509-624-4276; Fax 509-838-3424)
 Wells, St. John, Roberts, Gregory & Matkin P.S.
 601 West First Avenue, Suite 1300
 Spokane, WA 99201-3828

Responsive to the Notice To File Corrected Application Papers dated October 15, 2001, Applicant amends and remarks as shown in the specification, Substitute Drawing Request and formal drawings submitted herewith.

Replace the entire specification submitted to the U.S. Patent and Trademark Office on September 18, 2001 with the "clear" copy of the specification submitted herewith. Applicant apologizes for the quality of the specification submitted previously. This specification is a true and correct copy and no changes have been made other than the clarity of the copy.

Further, as shown by the enclosed copies of the Return Receipt Postcard and Ex Post Declaration of Express Mailing, Applicant submitted a Substitute